



IWSEC 2010



ISEC is a technical group on Information Security in ESS, Engineering Sciences Society, of IEICE, the Institute of Electronics, Information and Communication Engineers, Japan

Call for Papers
The 5th International Workshop on Security
November 22-24, 2010

CSEC is a special interest group on Computer Security of IPSJ, Information Processing Society of Japan

Kobe International Conference Center, Kobe, Japan

Web page: <http://www.iwsec.org/2010/>

Contact: iwsec2010@iwsec.org

Jointly organized by **ISEC** and **CSEC**

Original papers on the research and development of various security topics are solicited for submission to IWSEC 2010.

Important Dates

Submission Deadline (Extended)	May 10, 2010
Notification	July 14, 2010
IWSEC 2010 Workshop	November 22-24,

IWSEC 2010 will select submissions from three tracks, and the review and selection processes of these tracks will be independent of each other. **Track III (security in real life applications)** is a new track designed to allow researchers and practitioners to share their research and experience. We welcome papers that highlight the practical aspects of security technologies and industrial case studies. Topics of interest for the conference include but are not limited to:

Track I: Foundations of Security

Authentication	Cryptanalysis	Public Key Cryptosystems
Biometrics	Digital Signatures	Quantum Security
Block/Stream Ciphers	Hash Functions	
Cryptographic Protocols	Information Hiding	

Track II: Security in Networks and Ubiquitous Computing Systems

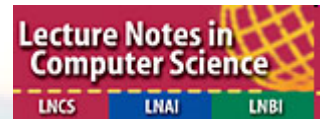
Network Security	Privacy Enhancing Technology
Intrusion Detection	Anonymity-related Technology
Malware Detection	Privacy Preserving System
Security in Sensor Networks	Privacy on WEB and DB
Security of Ubiquitous and Cloud Computing	Software Security
Security of Multi-party Computing	Security Architectures
Authorization and Access Control	Data and System Integrity
Mobile Security	Reliable Secure Interactions of Services

Track III: Security in Real Life Applications

Secure Living and Working Environments	Protection of Critical Infrastructures
Secure Consumer Electronics	Critical Infrastructure Protection
RFID Security	Risk Analysis and Risk Management
Security in Commerce and Government	Testing, Verification and Certification
DRM, Copyright Protection, and Content Security	Security Verification
E-business/e-commerce/e-government Security	Assurance
Security Management	Law, Policy, Ethics and Related Technologies
Access Control Policies and ISMS	Digital Forensics
Business Processes Security	Information Law and Ethics

Instructions for Authors

Submitted papers must be original, unpublished, and not submitted to other conferences or journals for consideration for publication. Papers must be written in English and be fully anonymous with no author names, affiliations, acknowledgements, or obvious references.



They should be at most 16 pages long in total, in standard LNCS format except for author names and affiliations. Papers should be sufficiently intelligible and self-contained without appendices so that PC members can review papers without them. Accepted full papers will be presented at the workshop and will be published in the LNCS conference proceedings series. Papers accepted as short papers will also be presented at the workshop and will be published in the locally printed proceedings with an official ISBN number. At least one author of each accepted paper is required to register with the workshop and present the paper. Each paper can be submitted to one of the three tracks. The review and selection processes will be done according to the policy of the track. For instance, Track III emphasizes the practicability of the technology.

Best Paper Awards

Prizes will be awarded to the authors of the best paper(s) and the best student paper(s).

General Chairs

Hiroaki Kikuchi Tokai University, Japan
Toru Fujiwara Osaka University, Japan

Program Chairs

Isao Echizen National Institute of Informatics, Japan
Noboru Kunihiro The University of Tokyo, Japan
Ryoichi Sasaki Tokyo Denki University, Japan

Program Committee

Track I: Foundations of Security (Chair: Noboru Kunihiro, The University of Tokyo, Japan)

Zhenfu Cao, China	Eiichiro Fujisaki, Japan	Aggelos Kiayias, Greece	Tetsu Iwata, Japan
Alfred Menezes, Canada	Phong Nguyen, France	Kazuo Ohta, Japan	Raphael Phan, UK
Bart Preneel, Belgium	Christian Rechberger, Belgium	Rei Safavi-Naini, Canada	Palash Sarkar, India
Willy Susilo, Australia	Tsuyoshi Takagi, Japan	Routo Terada, Brasil	Sung-Ming Yen, Taiwan
Yuliang Zheng, USA			

Track II: Security in Networks and Ubiquitous Computing Systems (Chair: Isao Echizen, NII, Japan)

Liqun Chen, UK	Bart De Decker, Belgium	William Enck, USA	Dieter Gollmann, Germany
Yoshiaki Hori, Japan	Angelos D. Keromytis, USA	Seungjoo Kim, Korea	Kwok-Yan Lam, China
Joseph Liu, Singapore	Javier Lopez, Spain	Kyung-Hyune Rhee, Korea	
Ahamd-Reza Sadeghi, Germany	Keisuke Takemori, Japan	Sven Wohlgenuth, Japan	Toshihiro Yamauchi, Japan
Hiroshi Yoshiura, Japan	Alf Zugenmaier, Germany		

Track III: Security in Real Life Applications (Chair: Ryoichi Sasaki, Tokyo Denki University, Japan)

Rafael Accorsi, Germany	Claudio Ardagna, Italy	Kevin Butler, USA	Pau-Chen Cheng, USA
Steven Furnell, UK	Jongsung Kim, Korea	Tetsutaro Kobayashi, Japan	Jigang Liu, USA
Masakatsu Nishigaki, Japan	Hartmut Pohl, Germany	Kai Rannenberg, Germany	Sujeet Sheno, USA
Reima Suomi, Finland	Mikiya Tani, Japan	Ryuya Uda, Japan	
Sabrina De Capitani di Vimercati, Italy		Guilin Wang, UK	

(Last Update: April 30, 2010)